UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/772,203	02/04/2004	David R. Strip	86842AJA	4247

7590 10/18/2005

Paul A. Leipold
Patent Legal Staff
Eastman Kodak Company
343 State Street
Rochester, NY 14650-2201

EXAMINER	
OLANDER, GABRIEL D	

ART UNIT	PAPER NUMBER
2879	

DATE MAILED: 10/18/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/772,203	STRIP, DAVID R.	
	Examiner	Art Unit	
	Gabriel D. Olander	2879	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *04 February 2004*.

2a)☐ This action is **FINAL**. 2b)☒ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-21* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-21* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☒ The specification is objected to by the Examiner.

10)☒ The drawing(s) filed on *04 February 2004* is/are: a)☒ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☒ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application (PTO-152)
6) ☐ Other: _____.

DETAILED ACTION

Specification

The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

The following title is suggested: A method of manufacturing flat panel light emitting devices comprising defect removal.

Claim Objections

Claim 5 is objected to under 37 CFR 1.75(c), as being of improper dependent form for failing to further limit the subject matter of a previous claim. Applicant is required to cancel the claim(s), or amend the claim(s) to place the claim(s) in proper dependent form, or rewrite the claim(s) in independent form. "a display" as stated in claim 5 does not limit "a display having a plurality of light emitting elements" as stated in claim 4.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-15 & 18-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Hiroki (US 2001/0048110).

Claim 1: Hiroki discloses a method of manufacturing a flat panel light emitting device of a predetermined size, comprising:

a) forming an area of light emitting materials on a substrate, the area being larger than the predetermined size (lines 59-67, column 4);

b) detecting defects in the area (fig. 1);

c) determining a defect free portion of the area having the predetermined size (fig. 1);

d) cutting the defect free portion from the substrate to produce the flat panel light emitting device (lines 33-45, column 9).

Claim 2: Hiroki discloses the method of claim 1, wherein the device is an area illumination light source (line 32, column 1).

Claim 3: Hiroki discloses the method of claim 2, wherein the light emitting area includes a plurality of light emitting elements that are connected in series (fig. 10).

Claims 4 & 5: Hiroki discloses the method of claim 1, wherein the device is a display having a plurality of light emitting elements (fig. 10).

Claim 6: Hiroki discloses the method of claim 4, wherein the device is area illumination light source (line 32, column 1).

Claim 7: Hiroki discloses the method of claim 5, wherein the light emitting elements are arranged in triplets that emit red, green, or blue light (lines 45-55, column 16).

Claim 8: Hiroki discloses the method of claim 1, wherein the substrate is a web (fig.1, 102).

Claim 9: Hiroki discloses the method of claim 1, wherein the substrate is a discrete sheet (fig. 1A, 102).

Claim 10: Hiroki discloses the method of claim 1, wherein the substrate is flexible (lines 30-41, column 1).

Claim 11: Hiroki discloses the method of claim 1, wherein the substrate is rigid (lines 30-41, column 1).

Claim 12: Hiroki discloses the method of claim 1, wherein the light emitting elements are OLEDs (lines 60-65, column 1).

Claim 13: Hiroki discloses the method of claim 1, wherein the light emitting area includes a plurality of identically shaped light emitting elements (fig. 1).

Claim 14: Hiroki discloses the method of claim 1, wherein the light emitting area includes a plurality of differently shaped light emitting elements (figs. 13-16).

Claim 15: Hiroki discloses the method of claim 1, wherein the light emitting elements are elongated strips (figs. 13a & 13d & 13f).

Claim 18: Hiroki discloses the method of claim 1, wherein the light emitting device includes electrical contacts (fig. 6a, 5074), and further comprising steps of

a. providing a cover over the light emitting device (fig. 6a, 5056), leaving electrical contacts extending beyond the cover, and

b. sealing the cover to the substrate to encapsulate the light emitting materials between the substrate and the cover (shown fig. 6a)

Claim 19: Hiroki discloses the method of claim 18, further comprising the step of removing overlying materials to extend the electrical contacts beyond the cover (lines 30-37, column 14).

Claim 20: Hiroki discloses the method of claim 1, wherein the light emitting materials defines a plurality of light emitting elements and further comprising the steps of:

a. providing electrical conductors between the light emitting elements and the periphery of the defect free portion (fig. 2a, s1-sx & v1-vx & g1-gx);

a. providing a cover over the light emitting device (fig. 6a, 5056), leaving electrical contacts extending beyond the cover, and

b. sealing the cover to the substrate to encapsulate the light emitting materials between the substrate and the cover (shown fig. 6a)

Claim 21: Hiroki discloses the method of claim 20, further comprising the step of removing overlying materials to extend the electrical contacts beyond the cover (lines 30-37, column 14).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

> (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 16 & 17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hiroki (US 2001/0048110).

Hiroki discloses all the limitations of claim 1, wherein the light emitting device can have one of a plurality of predetermined sizes (fig. 13-16).

Hiroki does not disclose the step of determining a maximum number of defect free portions that can be cut from the array or an determining an optimum arrangement of defect free portions.

In order to enable maximum use of said defect free portions, it would be obvious to one of ordinary skill in the art at the time of the invention to determine the maximum number of defect free portions that can be cut from the array as well as determine the optimum arrangement of defect free portions.

Contact Information

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gabriel D. Olander whose telephone number is 571-272-6011. The examiner can normally be reached on 8:30-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nimesh Patel can be reached on 571-272-2457. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

G.O.

Gabriel Olander

Patent Examiner

Art Unit 2879

10/17/05

MARICELI SANTIAGO
PRIMARY EXAMINER